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Docket: NECG 21.093 (100806-00258)  
Application: 10/815,120

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: YOSHITAKE et al.

Serial No.: 10/815,120

Art Unit: 2831

Filed: 3/26/2004

Examiner: THOMAS, Eric W.

Title: *Polarizing Electrode, Manufacturing Method Thereof, and Electric Double-Layer  
Capacitor*

October 10, 2006

**RESPONSE TO OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Commissioner:

In response to the Office Action mailed July 11, 2006, the Applicants submit the  
following:

Amendments to the **Drawings** are not being made by this paper.

Amendments to the **Specification** are not being made by this paper.

Amendments to the **Claims** begin on page 2 of this paper.

**Remarks** begin on page 6 of this paper.

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